

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)
Think Excellence. Live Excellence.
Shri VaishnavSM Institute of Technology and Science, Indore
Department of Electricals and Electronics Engineering



Time Table - Jul-Dec 2021

Doc No. : SVVV/AC/002		Rev No. :			Rev. Date :	
Class : II Yr. EC-RA		Block: Department of Electrical and Electronics Engineering			Room No. : Online Mode	
Effective From: 23 Aug 2020		Total Load / Week: 24			Class Teacher: Mr. Deepak Mittal	
TIME/DAY	Monday	Tuesday	Wednesday	Thursday	Friday	Saturday
L1	BTEC302	BTEC302	BTEC302	BTEC302 P	ML301	BTEC305
10:30 - 11:15	DM1	DM1	DM1	DM1	MV	NP1
L2	ML301	BTEC303	BTEC305 P	BTEC303	BTME303	BTME303
11:15 - 12:00	MV	DM	NP1	DM	ME	ME
L3	BTME303	ML301	BTME303	BTME303	BTEC305 P	BTEC302 T
12:00 - 12:45	ME	MV	ME	ME	NP1	DM1
L4	BTEC303	BTEC305 P	BTEC305 T	BTEC303 P	BTEC303	BTEC307 P
12:45 - 01:30	DM	NP1	NP1	DM	DM	PJ

L-Lecture, T-Tutorial, P-Practical

S No.	Sub. Code	Subject Name	L	T	P	C	Name of Faculty	Abb
1	ML301	Environment and Energy Studies	3	0	0	3	Mrs. Mausmi Verma	MV
2	BTEC302	Network Analysis and Synthesis	3	1	2	5	Mr. Dilip Mandloi	DM1
3	BTEC303	Electronic Measurement and Instrumentation	3	1	2	5	Mr. Deepal Mittal	DM
4	BTEC305	Electronic Circuits	3	1	2	5	Mr. Naresh Purohit	NP1
5	BTME303	Strength of Materials	3	1	2	5	Mr. Santosh Yadav	SY
6	BTRA301	PCB Designing Lab	0	0	2	1	Mr. Preet Jain	PJ
			15	4	10	24		

Mr. Shyam Gehlot
Mr. Vikas Jain
Time Table Coordinator
Dept. of EEE

Dr. Namit Gupta
HOD, Dept. of EEE
Director, SVITS

Dr. Shishir Jain
Time Table Coordinator
SVVV